Attorney Docket No.: MICRON-01023

DECLARATION FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name. I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: ON-CHIP SUBSTRATE REGULATOR TEST MODE. The specification of which (check one) x is attached hereto or was filed on as Application Serial No. and was amended on (if applicable). I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Phonty Claim	
Thor Poleign Application(s)			Yes No	
Number	Country	Day/Month/Year File	d	
I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:				
Application Serial No.	Filing Dat	e Status:	Patented, Pending, Abandoned	
I hereby declare that all statements made believed to be true; and further that these punishable by fine or imprisonment or be may jeopardize the validity of the applica- Full Name of Inventor: Gary Gilliam	e statements were made with the kooth, under Section 1001 of Title 1	nowledge that willful false statem 8 of the United States Code and to	ents and the like so made are	
Inventor's Signature:	Y Mary ise, Idaho 83709		8-16-95 Date	
Post Office Address: 8000 S. Federal	Way, Boise, Idaho 83706			

Dated: August 29, 1995

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Group:			
Gary Gilliam) Art Unit:) Examiner:			
Serial No.:)			
Filed: Filed Herewith) POWER OF ATTORNEY BY ASSIGNEE)			
For: ON-CHIP SUBSTRATE REGULATOR TEST MODE))			
Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231				
Sir:				
MICRON TECHNOLOGY, INC., Assignee of the above-identified application by Assignment dated 08/16/95 hereby appoints Lia M. Pappas, (Reg. No. 34,095) and also the members of the firm of				
HAVERSTOCK & ASSOCIATES a firm including Thomas B. Haverstock (Reg. No. 32,571) and Jonathan O.				
Owens (Reg. No. 37,902), 260 Sheridan Avenue, Suite 420, Palo Alto, California 94306, (415) 833-0160, as its				
attorneys with full power of substitution to prosecute this application and to transact all business in the Patent				
and Trademark Office in connection therewith.				
Please direct all correspondence regarding this application to the following:				
Thomas B. Haverstock HAVERSTOCK & ASSOCIATES 260 Sheridan Avenue, Suite 420 Palo Alto, California 94306 Telephone: (415) 833-0160 Facsimile: (415) 833-0170				
I hereby certify that the Assignment document filed with the application or filed subsequent to the filing date of the application, has been reviewed and I hereby certify that, to the best of my knowledge and belief, title is with MICRON TECHNOLOGY, INC.				
MICRON T	TECHNOLOGY, INC.			

By: W. Bryan Farney

Name: W. Bryan Farney

Title: Vice President of Legal Affairs and General Counsel

<u>S/N 08/520,818</u> PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Gary R. Gilliam

Examiner: Unknown

Serial No.:

08/520,818

Group Art Unit: 2213

Filed:

August 30, 1995

Docket: 303.221US1

Title:

ON-CHIP SUBSTRATE REGULATOR TEST MODE

REVOCATION/POWER OF ATTORNEY CERTIFICATE UNDER 37 CFR 3.73(b)

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Pappas, Lia M.

Schwegman, Micheal L.

In accordance with 37 C.F.R. Section 1.32, M.P.E.P. Section 402.05, 402.07, and 324 please revoke any existing Powers of Attorney, and appoint the following attorneys and/or patent agents to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith:

Slifer, Russell D.

Viksnins, Ann S.

Woessner, Warren D.

Bianchi, Timothy E. Reg. No. 39,610 Reg. No. 38,080 Billig, Patrick G. Reg. No. 32,836 Billion, Richard E. Reg. No. 35,075 Brennan, Thomas F. Burke, John E. Reg. No. 35,836 Clark, Barbara J. Reg. No. 38,107 Reg. No. 39,662 Dryja, Michael A. Embretson, Janet E. Reg. No. 39,665 Farney, W. Bryan Reg. No. 32,651 Reg. No. 35,138 Fogg, David N.

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